	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	0	(computer same bus same heirachical)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/04/10 12:48
2	BRS	L2	0	(computer same bus) and (heirachical same represent$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/04/10 12:49
3	BRS	L3	2	(heirachical same represent$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/04/10 12:50
4	BRS	L4	0	(heirachical same intermedia$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/04/10 12:52
5	BRS	L5	3	(heirachical same homogeneous)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/04/10 12:53

	Type	L #	Hits	Search Text	DBs	Time Stamp
6	BRS	L6	0	(heirachical adj intermediate)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/04/10 12:53
7	BRS	L7	12050	(application adj program adj interface)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/04/10 12:53
8	BRS	L8	614	(application adj program adj interface) and computer and (dynamic same modif$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/04/10 12:54
9	BRS	L9	469	(application adj program adj interface) and (dynamic same modif$4) and (computer same memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/04/10 12:54
10	BRS	L10	474250	(application adj program adj interface) and (dynamic same modif$4) and (computer same memory) and (system same bus) hetero$6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/04/10 12:55

	Type	L #	Hits	Search Text	DBs	Time Stamp
11	BRS	L11	32	(application adj program adj interface) and (dynamic same modif$4) and (computer same memory) and (system same bus) and hetero$6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/04/10 15:24
12	BRS	L12	418	717/108.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/04/10 13:47
13	BRS	L13	0	(hetrogeneous same programs)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/04/10 15:26
14	BRS	L14	3797	(application same interface same computer) and (navigat$4 same program)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/04/10 15:27
15	BRS	L15	3	(application same interface same computer) and (navigat$4 same program) and (query) and (thread same manag$4) and heterogeneous	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/04/10 15:28

	Type	L #	Hits	Search Text	DBs	Time Stamp
16	BRS	L16	0	(application same interface same computer) and (navigat$4 same program) and (query) and (thread same manag$4) and (heterogeneous near program)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/04/10 15:29
17	BRS	L17	0	(application same interface) and (navigat$4 same program) and (query) and (thread same manag$4) and (heterogeneous near program)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/04/10 15:29
18	BRS	L18	0	(application same interface) and (navigat$4 same program) and (query) and (thread adj manag$4) and (heterogeneous near program)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/04/10 15:31
19	BRS	L19	0	(thread adj manag$4) and (heterogeneous near program)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/04/10 15:29
20	BRS	L20	2	(thread same manag$4) and (heterogeneous near program)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/04/10 15:30

	Type	L #	Hits	Search Text	DBs	Time Stamp
21	BRS	L21	0	(thread same manag$4) and (heterogeneous near program) and hierarch$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/04/10 15:30
22	BRS	L22	0	(thread same manag$4) and (heterogeneous near program) and hierarch$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/04/10 15:30
23	BRS	L23	0	(navigat$4 same program) and (query) and (thread near manag$4) and (heterogeneous near program)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/04/10 15:31
24	BRS	L25	3	(navigat$4 same program) and (query) and (thread near manag$4) and heterogeneous and modif$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/04/10 15:34
25	BRS	L26	0	(navigat$4 same program) and (query) and (thread near manag$4) and heterogeneous and (modif$3 near function)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/04/10 15:34

	Type	L #	Hits	Search Text	DBs	Time Stamp
26	BRS	L24	4	(navigat$4 same program) and (query) and (thread near manag$4) and heterogeneous	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/04/10 15:40
27	BRS	L27	15	(hierarchical adj internal)	USPAT	2006/04/10 15:40
28	BRS	L28	28	(hierarchical adj internal)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/04/10 15:41
29	BRS	L29	0	(hierarchical adj internal adj representation)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/04/10 15:41
30	BRS	L30	10	(hierarchical adj internal) and (first same function$2) and (second same function$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/04/10 15:41
31	BRS	L31	0	(hierarchical adj internal) and (first same function$2) and (second same function$2) and (dynamic near heterogeneous)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/04/10 15:42

	Type	L #	Hits	Search Text	DBs	Time Stamp
32	BRS	L32	1	(hierarchical adj internal) and (first same function$2) and (second same function$2) and (dynamic same heterogeneous)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/04/10 15:42
33	BRS	L33	0	(hierarchical adj internal) and (first adj set$2) and (second adj set$2) and (dynamic same heterogeneous)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/04/10 15:43
34	BRS	L34	0	(hierarchical adj internal) and (first adj set$2) and (second adj set$2) and (set$2 same function$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/04/10 15:43
35	BRS	L35	0	(hierarchical adj internal) and (first adj set$2) and (second adj set$2) and (set same function)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/04/10 15:43
36	BRS	L36	0	(hierarchical adj internal) and (first adj set$2) and (second adj set$2) and (set near function)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/04/10 15:43

	Type	L #	Hits	Search Text	DBs	Time Stamp
37	BRS	L37	0	(hierarchical adj internal) and (first adj set$2) and (second adj set$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/04/10 15:44
38	BRS	L38	0	(block adj representation) and (procedure adj representation) and (program adj application)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/04/10 15:44
39	BRS	L39	0	(block adj representation) and (procedure adj representation)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/04/10 15:44

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  ```

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  ```
  Where are the sales offices?
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- Capitalize proper nouns to search for specific people, places, or products.

  ```
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  ```
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  ```

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  ```

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  ```
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  ```

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```
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```

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Edwards, W.J.;
Electron Devices, IEEE Transactions on
Volume 15, Issue 7, Jul 1968 Page(s):445 - 445
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2. **Foreword (June 1968)**
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3. **Foreword, July 1968**
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4. **Comments on `A zero crossing-based spectrum analyzer' by S.M. Kay an**
Edwards, P.J.;
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5. **The performance enhancement of multibeam adaptive base-station anten land mobile radio systems**
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☐ **25. A rework reduction model for construction projects**
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   Maogang wang; Xiaojian Yang; Sarrafzadeh, M.;
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Memory disambiguation to facilitate instruction-level parallelism compilation
DM Gallagher - 1995 - crhc.uiuc.edu
... **MEMORY** DISAMBIGUATION TO FACILITATE INSTRUCTION-LEVEL PARALLELISM COMPILATION BY ... **MEMORY** DISAMBIGUATION TO FACILITATE INSTRUCTION-LEVEL PARALLELISM COMPILATION ...
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A VHDL-AMS Compiler and Architecture Generator for Behavioral Synthesis of Analog Systems - group of 12 »
A Doboli, R Vemuri - DATE, 1999 - portal.acm.org
... as the behavior is strongly **heterogeneous** with respect ... VHIF (VASE **Hierarchical Intermediate** Format) [2] is a ... without relying on any state (**memory**) information. ...
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NHF Beebe - math.utah.edu
... **Heterogeneous** [MPT94, PR95]. Heuristic [KKY + 92, MPT94]. Heuristics [AC92, SKW92]. Hierarchical [BJWS96, GFNS99, JP99, RG93, SCS90]. Hierarchical-**Memory** [RG93 ...
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Enhancing Perception and Planning of Software Agents with Emotion and Acquired Hierarchical ...
J Bach - Proceedings of MASHO, 2002 - informatik.hu-berlin.de
... the agents is compositional and **heterogenous**, there is ... If schemas are **hierarchical**, **intermediate** schemas are ... Especially during **memory** retrieval, the analogy to ...
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[PS] Hardware Synthesis from C with Multiple Register Files in Data Path - group of 2 »
NK Agarwal - 2003 - cse.iitd.ernet.in
... generates the VHDL net list. Since the system consists of a **heterogeneous** environment, techniques and ... **Memory** ... This pass generates the **hierarchical intermediate** ...
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Automated Multi-Tier System Design for Service Availability - group of 6 »
GJ Janakiraman, JR Santos, Y Turner - 1st Workshop on Design of Self-Managing Systems (at DSN 200) ..., 2003 - hpl.hp.com
... As AVED creates the **hierarchical intermediate** ... anism for checkpointing the application state on remote peer **memory**, which is fast). ...
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[BOOK] System-on-chip methodologies & design languages
PJ Ashenden, JP Mermet, R Seepold - 2001 - books.google.com
... It was then suggested to keep the **memory** of the CHDL through a new series ofbooks that would put together the best papers of all 3 events in the domain of Chip ...
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... If schemas are **hierarchical**, **intermediate** schemas are equivalent to concept nodes. ...
Especially during **memory** retrieval, the analogy to CRNs with spreading ...
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Terms used **hierarchical intermediate representation**

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1 Session 4: compilers 1: Facilitating the search for compositions of program transformations

Albert Cohen, Marc Sigler, Sylvain Girbal, Olivier Temam, David Parello, Nicolas Vasilache

June 2005 **Proceedings of the 19th annual international conference on Supercomputing ICS '05**

Publisher: ACM Press

Full text available: pdf(365.49 KB) Additional Information: full citation, abstract, references

Static compiler optimizations can hardly cope with the complex run-time behavior and hardware components interplay of modern processor architectures. Multiple architectural phenomena occur and interact simultaneously, which requires the optimizer to combine multiple program transformations. Whether these transformations are selected through static analysis and models, runtime feedback, or both, the underlying infrastructure must have the ability to perform long and complex compositions of progra ...

2 Coordinated parallelizing compiler optimizations and high-level synthesis

Sumit Gupta, Rajesh Kumar Gupta, Nikil D. Dutt, Alexandru Nicolau

October 2004 **ACM Transactions on Design Automation of Electronic Systems (TODAES)**, Volume 9 Issue 4

Publisher: ACM Press

Full text available: pdf(923.65 KB) Additional Information: full citation, abstract, references, index terms

We present a high-level synthesis methodology that applies a coordinated set of coarse-grain and fine-grain parallelizing transformations. The transformations are applied both during a pre-synthesis phase and during scheduling, with the objective of optimizing the results of synthesis and reducing the impact of control flow constructs on the quality of results. We first apply a set of source level presynthesis transformations that include common sub-expression elimination (CSE), copy propagat ...

Keywords: Code motions, common subexpression elimination, dynamic CSE, embedded systems, high-level synthesis, parallelizing transformations, presynthesis

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